

AT32Fxx Series TMR One-Pulse Mode

Introduction

This example code demonstrates how to use TMR one-pulse mode in AT32Fxx series.

Note: This sample code is written based on Artery's V2.x.x BSP. For other versions of BSP, users should pay attention to the differences in use.

Applicable products:

Product series	AT32Fxx series
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List of major peripherals used:

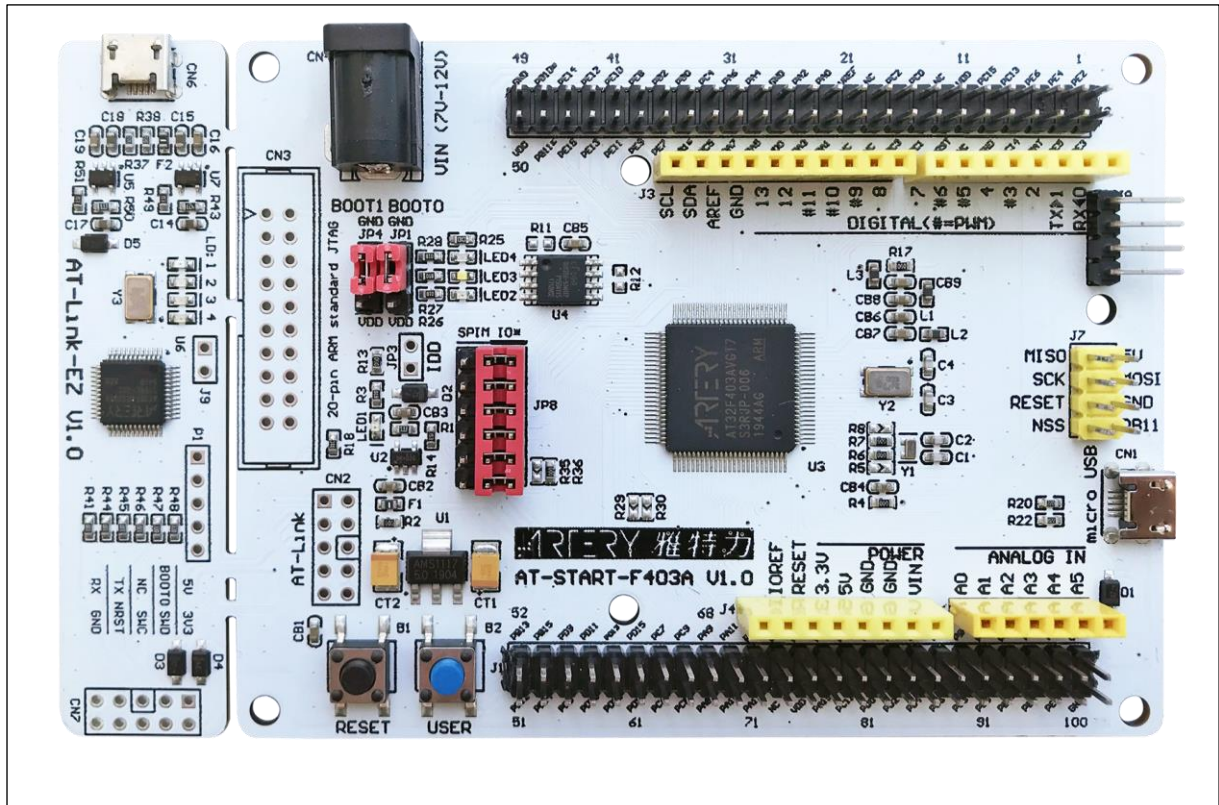
Peripherals	TMR
	GPIO

1 Quick start

1.1 Hardware resources

- 1) AT-START-F403A V1.0 evaluation board (use evaluation board according to certain specific product series)
- 2) PB6 for waveform output

Figure 1. AT-START-F403A V1.0 evaluation board



1.2 Software resources

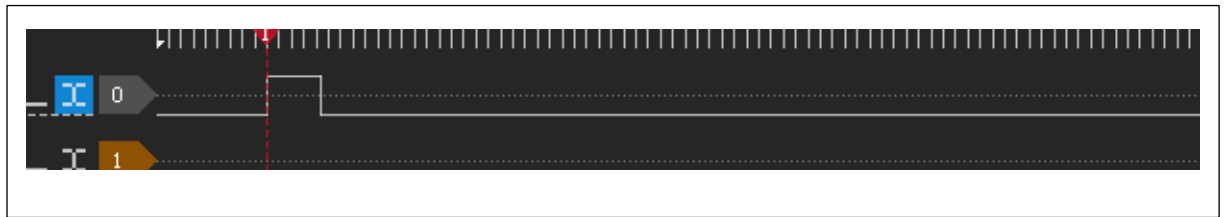
- 1) SourceCode
 - TMR_one_cycle

Note: All of projects are built based on Keil 5. For the need to run them in other compiling environments, user can make simple adjustments according to AT32xxx_Firmware_Library_V2.x.x\project\at_start_xxx\templates.

1.3 Example case

- 1) Open "TMR_one_cycle" source code, compile and download it to the board
- 2) AT-START-F403A V1.0 is used here, so we choose AT32F403A project here
- 3) For easier observation, we use a logic analyzer to capture waveforms on PB6

Figure 2. View result



2 Revision history

Table 1. Document revision history

Date	Revision	Changes
2022.1.26	2.0.0	Initial release

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